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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Joseph E. Geusic et al.

Title: INTEGRATED CIRCUITS USING OPTICAL WAVEGUIDE INTERCONNECTS FORMED THROUGH A SEMICONDUCTOR WAFER AND METHODS FOR FORMING SAME

Docket No.: 303.382US2
Filed: July 18, 2000
Examiner: Wai-Sing Louie



Serial No.: 09/618648
Due Date: August 23, 2003
Group Art Unit: 2814

Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450


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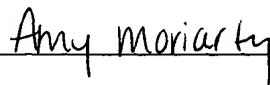
- ☒ A return postcard.
- ☒ An Amendment and Response (8 Pages).
- ☒ A Copy of the Information Disclosure Statement, 1449 Form, and English Abstract of JP 56-055067 filed on January 9, 2002. (5 pgs.).
- ☒ A Copy of the Terminal Disclaimer that was filed July 22, 2002 (2 pgs.).
- ☒ A Copy of the Terminal Disclaimer that was filed August 12, 2003 (3 pgs.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
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